EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L6	1147	pass transistor and driver circuit	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	ADJ	ON	2007/06/16 13:20
L7	438	L6 and pmos and nmos	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	ADJ	ON	2007/06/16 13:20
L8	54937	gate adj array	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/16 13:20
L9	1755	L8 and nmos and pmos	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/16 13:20
L10	67	L8 and nmos near3 pmos and nmos near3 series and pmos near3 series	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/16 13:20
L11	67	L10 and series	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/16 13:20
L12	829	L8 and soi	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/16 13:20
L13	3597	logic adj circuit near3 (driving drive driven)	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	OFF	2007/06/16 13:20
L14	11	pass transistor and driver circuit same (data (retaining retention or storage) or cache)	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	ADJ	ON	2007/06/16 13:20

EAST Search History

L15	109	cmos near3 pass adj transistor and series	US-PGPUB; USPAT	OR	OFF	2007/06/16 13:20
L16	1765	((257/204,206,208) or (365/174)). CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/16 13:20
L17	29	pass transistor and driver circuit same (data (retaining or retention or storage) or cache)	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	ADJ	ON	2007/06/16 13:20
L18	28	pass transistor and driver circuit same (data (retention or storage) or cache)	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	ADJ	ON	2007/06/16 13:20
L19	4	(("5187483") or ("5237184") or ("6559700") or ("6636073")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/16 13:20
L20	14	"571270"	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	OR	OFF	2007/06/16 13:20
L21	9	yonemaru.in. and series	US-PGPUB; USPAT	OR	OFF	2007/06/16 13:20
L22	36	L7 and pass transistor.clm. and driver circuit.clm.	US-PGPUB; USPAT; EPO; DERWENT; IBM_TDB	ADJ	ON	2007/06/16 13:20
L23	2	(US-20040140483-$ or US-20020053936-$ or US-20020070409-$).did.	US-PGPUB	OR	OFF	2007/06/16 13:20
L24	25	data and Yonemaru.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/16 13:20
L29	1058	L9 and series	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/16 13:20

L30	996	gate with accumulation with channel	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/16 13:20
L31	5243	gate with accumulation	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/16 13:20
L32	122	gate with accumulation with channel with (invert inverting inversion)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/16 13:20
L33	389	clocked with inverter with series	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/16 13:20
L34	12	clocked with inverter with series and pass adj transistor	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/16 13:20
L35	8	clocked with inverter with series and pass adj transistor and clocked adj inverter with output	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/16 13:20
L36	7	clocked with inverter with series.detx. and pass adj transistor.detx. and clocked adj inverter with output.detx.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/16 13:20
L37	12	barrel adj shifter with (cmos or (nmos with pmos)).detx.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/16 13:20

L38	16	(US-20050182809-$ or US-20020070409-$ or US-20040140483-$ or US-20020053936-$ or US-20040071013-$ or US-20020057111-$).did. or (US-6794916-$ or US-5457723-$ or US-6329845-$ or US-5719803-$ or US-5130940-$ or US-5602406-$ or US-5808483-$ or US-5581202-$ or US-5583457-$ or US-5424656-$).did.	US-PGPUB; USPAT	OR	OFF	2007/06/16 13:20
L39	12	clocked with inverter with series and pass adj transistor	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/16 13:20
L40	7	clocked with inverter with series.detx. and pass adj transistor.detx. and clocked adj inverter with output.detx.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/16 13:20
L41	12	barrel adj shifter with (cmos or (nmos with pmos)).detx.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/16 13:20
L42	10	(L39 L40 L41) and inverter with (standard conventional prior)	US-PGPUB; USPAT	OR	OFF	2007/06/16 13:20
L43	2	L38 and conventional$3 with clock$2	US-PGPUB; USPAT	OR	OFF	2007/06/16 13:20
L44	4	L38 and inverter near clock$2	US-PGPUB; USPAT	OR	OFF	2007/06/16 13:20
L45	3	"383,478" and chren	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/16 13:20
L46	3	(10/383,478 "383,478") and chren	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/16 13:20

L47	3	("10383478" "383,478") and chren	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/16 13:20
L48	4	("09383478" "09/383478" "383,478") and chren	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/06/16 13:20